

FORM PTO-892 (REV. 3-78)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		SERIAL NO. <u>869851</u> <u>458,507</u>		GROUP/ART UNIT <u>236</u>		ATTACHMENT TO PAPER NUMBER <u>4</u>					
NOTICE OF REFERENCES CITED				APPLICANT(S) <u>Hori et al</u>									
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*		DOCUMENT NO.					DATE	NAME	CLASS	SUB-CLASS	FILING DATE IF APPROPRIATE		
	A	3	5	8	8	6	7	5	6-71	Sazuki	323	303	
	B	4	4	3	3	2	5	2	2-84	Lewis	307	269	1-82
	C												
	D												
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	G												
	H												
	I												
	J												
	K												
FOREIGN PATENT DOCUMENTS													
*		DOCUMENT NO.					DATE	COUNTRY	NAME	CLASS	SUB-CLASS	PERTINENT SHTS. DWG.	PP. SPEC.
	L												
	M												
	N												
	O												
	P												
	Q												
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)													
	R	<u>Buchsbaum, W., Encyclopedia of Integrated Circuits, Prentice-Hall, 1987,</u> <u>pp. 65-67, 113, 114.</u>											
	S												
	T												
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EXAMINER								DATE					
<u>Stephen Baker</u>								<u>7/18/90</u>					
* A copy of this reference is not being furnished with this office action. (See Manual of Patent Examining Procedure, section 707.05 (a).)													